

Current status of development of the high-speed digital processing system by ASIC for HEP-e on board the ERG satellite

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ERG (Energization and Radiation in Geospace) satellite will be launched in 2015 to understand the acceleration process of relativistic electrons and dynamical variations of the space storm in the inner magnetosphere. In efforts to understand the cross-energy coupling process generating relativistic electrons, the satellite is equipped with instruments for comprehensively observing plasma/particles, fields and waves. The Plasma and Particle Experiment (PPE) utilizes four electron sensors and two ion sensors in order to cover the wide energy range. HEP-e is one of the four electron sensors and uses sets of SSSD (Single-sided Silicon Strip Detector) to detect energetic electrons. HEP on board MMO (Mercury Magnetospheric Orbiter) also employs an ASIC called VATA for read-out system from the detector, but HEP-e on board the ERG satellite aims at handling data with higher speed and has VATA which can process simultaneously signals from 32 channels with ADC function. We present the current status of development of the high-speed digital processing system for HEP-e on board the ERG satellite.

Keywords: ERG, HEP-e, ASIC